# Extending Pluto-Style Polyhedral Scheduling with Consecutivity

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#### Outline



#### Introduction

- Consecutivity Concept
- Pluto-Style Polyhedral Scheduling
- Consecutivity Criterion
- Related Work

#### Intra-Statement Consecutivity

- Consecutivity Criterion
- Specifying Schedule Constraints
- Transformation to Constraints on Schedule Coefficients
- Solving Constraints on Schedule Coefficients (isl)
- Inter-Statement Consecutivity
- Local Rescheduling
- Conclusions and Future Work

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- 4 Local Rescheduling
- 5 Conclusions and Future Work

#### • Temporal Locality

Consecutive operations access the same memory element

⇒ reuse of data in cache or registers



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Spatial Locality

Consecutive operations access neighboring memory elements

- ⇒ reuse of cache lines
  - Temporal Locality
     Consecutive operations access
     the same memory element
    - ⇒ reuse of data in cache or registers



memory





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#### Consecutivity

Consecutive operations access consecutive memory elements

- $\Rightarrow$  vectorization
- ⇒ hardware cache prefetcher
- $\Rightarrow$  burst accesses, e.g., on FPGA (Xilinx)



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## Burst Accesses (Sketch)

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## Burst Accesses (Sketch)

```
AA = burst read start(A. N):
for (int i = 0; i < N; ++i) {
  BB = burst_read_start(B, M);
  for (int j = 0; j < M; ++j) {
                           C[i][i] =
      burst_read_iter(AA, &A[i]) *
      burst_read_iter(BB, &B[j]);
  }
  burst_read_end(BB, M);
}
burst_read_end(AA, N);
```

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#### Burst Accesses (Sketch)

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## Burst Accesses (Sketch)

```
CC = burst_write_start(C, M * N);
BB = burst read start(B, M):
for (int j = 0; j < M; ++j) {
  AA = burst_read_start(A, N);
  for (int i = 0; i < N; ++i) {
    burst_write_iter(CC, &C[j][i]) =
      burst_read_iter(AA, &A[i]) *
      burst_read_iter(BB, &B[j]);
  }
  burst_read_end(AA, N);
}
burst_read_end(BB, M);
burst_write_end(CC, M * N);
```

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## Pluto-Style Polyhedral Scheduling

A schedule assigns an execution order to statement instances

- original schedule (if any) derived from input
- target schedule computed by scheduler

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- original schedule (if any) derived from input
- target schedule computed by scheduler
- A polyhedral scheduler computes schedule using polyhedral model
  - instance set: set of schedulable statement instances
  - access relations: map instances to memory locations
  - dependence relations:
    - $\Rightarrow$  pairs of instances that need to be executed in order
    - $\Rightarrow$  derived from access relations and original schedule

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#### A polyhedral scheduler computes schedule using polyhedral model

Result (typically):

- multiple (quasi) affine functions on instance set
- hierarchically organized (sequence, tree)

Types:

- Farkas based schedulers (Feautrier 1992)
  - ⇒ use Farkas to transform dependences into constraints on schedule coefficients
    - Pluto-style schedulers, e.g., Pluto, isl ⇒ compute affine functions one by one

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- temporal locality
- permutability  $\Rightarrow$  tiling

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Remaining freedom (if any)

 $\Rightarrow$  isl scheduler tends towards lexicographic ordering of instances

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Extreme example:

for (i=0; ifor (j=0; jI: B[j][i] = 0;  
$$T[i,j] \rightarrow [i,j]$$
  
not consecutive

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Goal: steer towards consecutivity in case of sufficient freedom Current implementation in isl (roughly): permutability > parallelism > consecutivity > temporal locality.

#### **Consecutivity Criterion**

Consecutive operations access consecutive memory elements

Assume (for the purpose of consecutivity)

- intra-statement consecutivity ( $\Rightarrow$  per statement)
- row-major array layout
- purely affine access function F
- purely affine per-statement schedule T

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Transformed access function  $F T^{-1}$  exhibits consecutivity if

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$$[\ldots + 0i_n] \ldots [\ldots + 0i_n][\ldots + 1i_n]$$



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- purely affine access function F = [G; H]
- purely affine per-statement schedule  $T = [T_1; T_2]$

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## Consecutivity Criterion and Spatial Locality

Consecutivity

$$F T^{-1} = \begin{bmatrix} M & \mathbf{0} \\ N & 1 \end{bmatrix}$$

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# Consecutivity Criterion and Spatial Locality

$$F T^{-1} = \begin{bmatrix} M & \mathbf{0} \\ N & x \end{bmatrix}$$

Consecutivity

$$F T^{-1} = \begin{bmatrix} M & \mathbf{0} \\ N & 1 \end{bmatrix}$$

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# Consecutivity Criterion and Spatial Locality

$$F T^{-1} = \begin{bmatrix} M & \mathbf{0} \\ N & x \end{bmatrix}$$

• Temporal Locality •  $T^{-1} = \begin{bmatrix} M & 0 \\ N & 0 \end{bmatrix}$ • Consecutivity  $F T^{-1} = \begin{bmatrix} M & 0 \\ N & 1 \end{bmatrix}$ 

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$$F T^{-1} = \begin{bmatrix} M & \mathbf{0} \\ N & x \end{bmatrix}$$

• Temporal Locality  $F T^{-1} = \begin{bmatrix} M & \mathbf{0} \\ N & \mathbf{0} \end{bmatrix}$ • Consecutivity  $F T^{-1} = \begin{bmatrix} M & \mathbf{0} \\ N & \mathbf{0} \end{bmatrix}$ 

- in case of innermost temporal locality
- $\Rightarrow$  consecutivity on next innermost loop iterator

$$F T^{-1} = \begin{bmatrix} M & \mathbf{0} & \mathbf{0} \\ N & 1 & \mathbf{0} \end{bmatrix}$$

(Kandemir, Ramanujam, and Choudhary 1999) - (B) (E) (E) (E) (E)

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### Related Work on Spatial Locality

Loop nest transformations (not per-statement)

- Wolf and Lam (1991)
  - ► define temporal (ker F) and spatial (ker G) reuse directions
  - partition original loop iterators
- Kandemir, Ramanujam, and Choudhary (1999)
  - aim: spatial locality
  - criterion more strict than required (ensures consecutivity)
  - incrementally fix elements of T<sup>-1</sup>
- Kandemir, Ramanujam, Choudhary, and Banerjee (2001)
  - ▶ pick (second to) last column of T<sup>-1</sup> from ker G

## **Related Work on Spatial Locality**

Per-statement schedulers

- Bastoul and Feautrier (2004)
  - ▶ pick proto-schedule T orthogonal to element from ker G (or ker F)
  - construct valid schedule C T
  - imposing constraints on linear combinations
     not directly applicable in isl
- Vasilache et al. (2012)
  - aim: spatial locality (ker  $T_1 \subseteq \ker G$ )
  - one-shot scheduler called multiple times
  - soft constraints encoded in ILP
- Pluto (2012) post scheduling intra-tile interchange
- Kong et al. (2013)
  - aim: consecutivity (stride-1 or stride-0)
  - partition original loop iterators
  - soft constraints encoded in ILP
- Zinenko et al. (2018)
  - spatial locality through spatial proximity constraints
  - soft constraints encoded in ILP

## Limitations

- partition original loop iterators Kong et al. (2013)
  - loop iterators in outer index expressions appear in outer schedule rows
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  - consecutivity requires innermost index expression to be equal to innermost schedule row (+ linear combinations of outer schedule rows)
  - how to handle iterators that appear in both?

for (int i = 0; i < M; ++i)
for (int j = 0; j < N; ++j)
S: A[j][j - i] = f(i, j);</pre>

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Other approaches, e.g., using  $S[i, j] \rightarrow [j, -i]$ :

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### Limitations

- post-schedule interchange
  - does not perform reversal, skewing
  - does not differentiate between statements
  - does not affect shape of schedule (e.g., distribution)

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```
void trps(int N, __pencil_consecutive float A[N][N],
   __pencil_consecutive float C[N][N])
{
   float tmp[N][N];
   for (int i = 0; i < N; i++)
     for (int j = 0; j < N; j++) {
     S: tmp[i][j] = A[i][j];
     T: C[j][i] = tmp[i][j];
     }
}
```

- without consecutivity:
  - ⇒ temporal locality on tmp prevents loop distribution
- with consecutivity:
  - ⇒ consecutivity requires different transformation per statement
  - $\Rightarrow$  loop distribution

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Transformed access function  $F T^{-1}$  exhibits consecutivity if

- outer index expressions independent of innermost loop iterator
- innermost index expression proportional to innermost loop iterator

$$F T^{-1} = \begin{bmatrix} G \\ H \end{bmatrix} \begin{bmatrix} T_1 \\ T_2 \end{bmatrix}^{-1} = \begin{bmatrix} M & \mathbf{0} \\ N & \mathbf{1} \end{bmatrix}$$

•  $G \mathbf{q} = \mathbf{0}$  (with  $\mathbf{q}$  the final columns of  $T^{-1}$ ) Note:  $\begin{bmatrix} T_1 \\ T_2 \end{bmatrix} T^{-1} = \begin{bmatrix} I & \mathbf{0} \\ \mathbf{0}^t & 1 \end{bmatrix}$   $\Rightarrow \mathbf{q}$  spans ker  $T_1$   $\Rightarrow \text{ ker } T_1 \subseteq \text{ ker } G$  (Vasilache et al. 2012) That is, rows of G need to be linear combinations of rows of  $T_1$  $G = A T_1$ 

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(with **q** the final columns of  $T^{-1}$ ) • Gq = 0 Note:  $\begin{bmatrix} T_1 \\ T_2 \end{bmatrix} T^{-1} = \begin{bmatrix} I & \mathbf{0} \\ \mathbf{0}^t & 1 \end{bmatrix}$  $\Rightarrow$  **q** spans ker  $T_1$  $\Rightarrow$  ker  $T_1 \subseteq$  ker G(Vasilache et al. 2012) That is, rows of G need to be linear combinations of rows of  $T_1$  $G = A T_1$ • *H***q** = 1

 $H = T_2 + B T_1$ 

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- outer index expressions independent of innermost loop iterator
- innermost index expression proportional to innermost loop iterator

$$F T^{-1} = \begin{bmatrix} G \\ H \end{bmatrix} \begin{bmatrix} T_1 \\ T_2 \end{bmatrix}^{-1} = \begin{bmatrix} M & \mathbf{0} \\ N & \mathbf{1} \end{bmatrix}$$

•  $G \mathbf{q} = \mathbf{0}$  (with  $\mathbf{q}$  the final columns of  $T^{-1}$ ) Note:  $\begin{bmatrix} T_1 \\ T_2 \end{bmatrix} T^{-1} = \begin{bmatrix} I & \mathbf{0} \\ \mathbf{0}^t & 1 \end{bmatrix}$   $\Rightarrow \mathbf{q}$  spans ker  $T_1$   $\Rightarrow \text{ ker } T_1 \subseteq \text{ ker } G$  (Vasilache et al. 2012) That is, rows of G need to be linear combinations of rows of  $T_1$   $G = A T_1$ •  $H \mathbf{q} = 1$ 

 $H = T_2 + B T_1$ 

 $\Rightarrow$  H needs to be linearly independent of G

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## **Multiple References**

single reference per statement
 Consecutivity constraint equal to index expression

$$F = \begin{bmatrix} G \\ H \end{bmatrix}$$

given

H linearly independent of G

Goal:

- G linear combination of outer schedule rows:  $G = A T_1$
- *H* equal to innermost schedule row :  $H = T_2 + B T_1$

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 $F = \begin{bmatrix} G \\ H \end{bmatrix}$ 

given

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- G linear combination of outer schedule rows:  $G = A T_1$
- *H* equal to innermost schedule row :  $H = T_2 + B T_1$
- multiple references per statement
  - $\Rightarrow$  potential conflicts

Possible resolutions:

- maximize number of satisfied consecutivity constraints
- consider constraints in order specified by user

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• single reference per statement

Consecutivity constraint equal to index expression

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given

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Possible resolutions:

- maximize number of satisfied consecutivity constraints
- consider constraints in order specified by user

## **Multiple References**

• single reference per statement

Consecutivity constraint equal to index expression

$$F = \begin{bmatrix} G \\ H \end{bmatrix}$$

given

- H linearly independent of G
- rows of H linearly independent

Goal:

- G linear combination of outer schedule rows:  $G = A T_1$
- *H* equal to innermost schedule rows:  $H = T_2 + B T_1$
- multiple references per statement
  - $\Rightarrow$  potential conflicts

Possible resolutions:

- maximize number of satisfied consecutivity constraints
- consider constraints in order specified by user
  - $\Rightarrow$  some constraints may be combined constraints with multi-row H

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## Multiple References Example: Matrix Multiplication

## Multiple References Example: Matrix Multiplication

for (int i = 0; i < N; ++i)
for (int j = 0; j < M; ++j)
for (int k = 0; k < K; ++k)
C[i][j] += A[i][k] \* B[k][j];
$$F_A = \begin{bmatrix} 1 & 0 & 0 \\ 0 & 0 & 1 \end{bmatrix} \quad F_B = \begin{bmatrix} 0 & 0 & 1 \\ 0 & 1 & 0 \end{bmatrix} \quad F_C = \begin{bmatrix} 1 & 0 & 0 \\ 0 & 1 & 0 \end{bmatrix}$$

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## Multiple References Example: Matrix Multiplication

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$$F_A = \begin{bmatrix} 1 & 0 & 0 \\ 0 & 0 & 1 \end{bmatrix} \quad F_B = \begin{bmatrix} 0 & 0 & 1 \\ 0 & 1 & 0 \end{bmatrix} \quad F_C = \begin{bmatrix} 1 & 0 & 0 \\ 0 & 1 & 0 \end{bmatrix}$$

$$F_{BC} = \begin{bmatrix} 0 & 0 & 1 \\ 1 & 0 & 0 \\ 0 & 1 & 0 \end{bmatrix}$$

## Multiple References Example: Matrix Multiplication

for (int i = 0; i < N; ++i)
for (int j = 0; j < M; ++j)
for (int k = 0; k < K; ++k)
 C[i][j] += A[i][k] \* B[k][j];

$$F_{A} = \begin{bmatrix} 1 & 0 & 0 \\ 0 & 0 & 1 \end{bmatrix} \quad F_{B} = \begin{bmatrix} 0 & 0 & 1 \\ 0 & 1 & 0 \end{bmatrix} \quad F_{C} = \begin{bmatrix} 1 & 0 & 0 \\ 0 & 1 & 0 \end{bmatrix}$$

$$F_{BC} = \begin{bmatrix} 0 & 0 & 1 \\ 1 & 0 & 0 \\ 0 & 1 & 0 \end{bmatrix} \quad F_{ABC} = \begin{bmatrix} 1 & 0 & 0 \\ 0 & 0 & 1 \\ 0 & 1 & 0 \end{bmatrix}$$

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## Multiple References Example: Matrix Multiplication

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for (int i = 0; i < N; ++i)  
for (int j = 0; j < M; ++j)  
for (int k = 0; k < K; ++k)  
C[i][j] += A[i][k] \* B[k][j];  

$$F_{A} = \begin{bmatrix} 1 & 0 & 0 \\ 0 & 0 & 1 \end{bmatrix} \quad F_{B} = \begin{bmatrix} 0 & 0 & 1 \\ 0 & 1 & 0 \end{bmatrix} \quad F_{C} = \begin{bmatrix} 1 & 0 & 0 \\ 0 & 1 & 0 \end{bmatrix}$$

$$F_{BC} = \begin{bmatrix} 0 & 0 & 1 \\ 1 & 0 & 0 \\ 0 & 1 & 0 \end{bmatrix} \quad F_{ABC} = \begin{bmatrix} 1 & 0 & 0 \\ 0 & 0 & 1 \\ 0 & 1 & 0 \end{bmatrix}$$

List:  $F_{ABC}$ ,  $F_{AC}$ ,  $F_{AB}$ ,  $F_{BC}$ ,  $F_A$ ,  $F_B$ ,  $F_C$ 

## **Multiple Final Rows**

• single final row

$$F T^{-1} = \begin{bmatrix} M & \mathbf{0} \\ N & 1 \end{bmatrix} \quad \text{or} \quad F T^{-1} = \begin{bmatrix} M & \mathbf{0} & \mathbf{0} \\ N & 1 & \mathbf{0} \end{bmatrix}$$

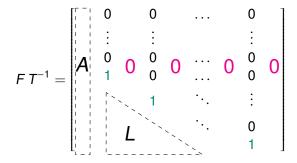
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## **Multiple Final Rows**

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$$F T^{-1} = \begin{bmatrix} M & \mathbf{0} \\ N & 1 \end{bmatrix} \quad \text{or} \quad F T^{-1} = \begin{bmatrix} M & \mathbf{0} & \mathbf{0} \\ N & 1 & \mathbf{0} \end{bmatrix}$$

multiple final rows



- multiple levels of consecutivity
- multiple levels of temporal locality (optional)

# Constraints on Schedule Coefficients

Affine schedule row:

$$f_{S}(\mathbf{x}) = \boxed{C_{S}} \mathbf{x} + \mathbf{d}_{S}$$

Constraints:

- validity: Farkas  $\rightarrow$  constraints on  $C_S$  and  $d_S$
- proximity (temporal locality):
   Farkas → constraints on C<sub>S</sub> and d<sub>S</sub>
- coincidence (parallelism):
   Farkas → constraints on C<sub>S</sub> and d<sub>S</sub>
- linear independence of previous rows  $(T_{S,0})$ :
  - $\Rightarrow$  compute orthogonal complement of  $T_{S,0}$ :  $U_S T_{S,0}^t = \mathbf{0}$
  - $\Rightarrow$  impose  $U_S C_S^t \neq \mathbf{0}$

 $f_T(\mathbf{y}) - f_S(\mathbf{x}) \ge 0$ 

- $f_T(\mathbf{y}) f_S(\mathbf{x})$  small
  - $f_T(\mathbf{y}) f_S(\mathbf{x}) = 0$ 
    - $C_S \neq YT_{S,0}$

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## Constraints on Schedule Coefficients for Consecutivity

- G linear combination of outer schedule rows:  $G = A T_1$
- *H* equal to innermost schedule rows:  $H = T_2 + B T_1$

- G linear combination of outer schedule rows:  $G = A T_1$
- *H* equal to innermost schedule rows:  $H = T_2 + B T_1$

#### Three stages

- G is not yet a linear combination of  $T_0$ 
  - $\Rightarrow take linear combination of G and T_0$ (heuristic to make progress)
  - $\Rightarrow$  but linearly independent of H and  $T_0$

$$\boldsymbol{C} = \boldsymbol{X} \begin{bmatrix} \boldsymbol{T}_0 \\ \boldsymbol{G} \end{bmatrix} \land \boldsymbol{C} \neq \boldsymbol{Y} \begin{bmatrix} \boldsymbol{T}_0 \\ \boldsymbol{H} \end{bmatrix}$$

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- G linear combination of outer schedule rows:  $G = A T_1$
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- **2** G is linear combination of  $T_0$ 
  - $\Rightarrow$  take C equal to next row of H

 $C = X \begin{vmatrix} T_0 \\ G \end{vmatrix} \land C \neq Y \begin{vmatrix} T_0 \\ H \end{vmatrix}$ 

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$$C = H_h + X \begin{bmatrix} T_1 \\ H_{< h} \end{bmatrix}$$

- G linear combination of outer schedule rows:  $G = A T_1$
- *H* equal to innermost schedule rows:  $H = T_2 + B T_1$

#### Three stages

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  - $\Rightarrow take linear combination of G and T_0$ (heuristic to make progress)
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- all rows of H have been handled
  - $\Rightarrow$  no further constraints (final zero columns in  $F T^{-1}$ )

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#### Three stages

- G is not yet a linear combination of  $T_0$ 
  - $\Rightarrow \text{ take linear combination of } G \text{ and } T_0$ (heuristic to make progress)
  - $\Rightarrow$  but linearly independent of H and  $T_0$
- **2** G is linear combination of  $T_0$ 
  - $\Rightarrow$  take C equal to next row of H
- all rows of H have been handled
  - $\Rightarrow$  no further constraints (final zero columns in  $F T^{-1}$ )
- At any stage
  - C may also be
    - linearly independent of  $T_0$ , G and  $H_{--}$

(intermediate zero columns in  $F T^{-1}$ )

• *C* of lower-dimensional statement may be linear combination of *T*<sub>0</sub>

$$\boldsymbol{C} = \boldsymbol{X} \begin{bmatrix} \boldsymbol{T}_0 \\ \boldsymbol{G} \end{bmatrix} \land \boldsymbol{C} \neq \boldsymbol{Y} \begin{bmatrix} \boldsymbol{T}_0 \\ \boldsymbol{H} \end{bmatrix}$$

$$C = H_h + X \begin{bmatrix} T_1 \\ H_{< h} \end{bmatrix}$$

 $C \neq Y \begin{bmatrix} T_0 \\ G \\ H \end{bmatrix}$ 

 $C = XT_0$ 

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## Solving Constraints on Schedule Coefficients (is1)

- validity, proximity, coincidence
  - $\Rightarrow$  encoded in ILP
- Iinear independence

$$C \neq YT_0 \qquad \rightarrow U C^t \neq \mathbf{0}$$

- ⇒ not linear
- $\Rightarrow$  backtracking search (in isl):  $U_i C^t \ge 1$  or  $U_i C^t \le -1$

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- ⇒ backtracking search (in isl):  $U_i C^t \ge 1$  or  $U_i C^t \le -1$
- consecutivity

$$C = X \begin{bmatrix} T_0 \\ G \end{bmatrix} \longrightarrow U'C^t = \mathbf{0} \qquad \text{linear}$$
$$C \neq Y \begin{bmatrix} T_0 \\ H \end{bmatrix} \longrightarrow U''C^t \neq \mathbf{0} \qquad \text{backtracking}$$

Note:

- ▶ extra rows  $H \Rightarrow$  fewer rows in  $U'' \Rightarrow$  fewer backtracking cases
- no extra ILP variables, but possibly more backtracking

## Solving Constraints on Schedule Coefficients (is1)

- validity, proximity, coincidence
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- ⇒ not linear
- ⇒ backtracking search (in isl):  $U_i C^t \ge 1$  or  $U_i C^t \le -1$
- consecutivity

$$C = X \begin{bmatrix} T_0 \\ G \end{bmatrix} \longrightarrow U'C^t = \mathbf{0} \qquad \text{linear}$$
$$C \neq Y \begin{bmatrix} T_0 \\ H \end{bmatrix} \longrightarrow U''C^t \neq \mathbf{0} \qquad \text{backtracking}$$

Note:

- extra rows  $H \Rightarrow$  fewer rows in  $U'' \Rightarrow$  fewer backtracking cases
- no extra ILP variables, but possibly more backtracking

Differences with linear independence handling:

- optional
- fixed part that applies in each backtracking case
- disjunctive (independent or dependent rows)
- ► conditional (multiple consecutivity constraints) ►

## Outline

## Introduction

- Consecutivity Concept
- Pluto-Style Polyhedral Scheduling
- Consecutivity Criterion
- Related Work

#### Intra-Statement Consecutivity

- Consecutivity Criterion
- Specifying Schedule Constraints
- Transformation to Constraints on Schedule Coefficients
- Solving Constraints on Schedule Coefficients (is1)

### Inter-Statement Consecutivity

- 4 Local Rescheduling
- 5 Conclusions and Future Work

}

### Inter-Statement Consecutivity

```
Input:
for (int i = 0; i < N; i += 2)
    for (int j = 0; j < M; j += 2) {
        B[j + 0][i + 0] = A[i + 0][j + 0];
        B[j + 1][i + 0] = A[i + 0][j + 1];
        B[i + 0][i + 1] = A[i + 1][j + 0];
        B[i + 1][i + 1] = A[i + 1][j + 1];
    }
Output (try and obtain distances 0 and 1):
for (int c0 = 0; c0 < M - 1; c0 += 2) {
  for (int c1 = 0; c1 < N - 1; c1 += 2) {
    B[c0][c1] = A[c1][c0];
    B[c0][c1 + 1] = A[c1 + 1][c0];
  }
  for (int c1 = 0; c1 < N - 1; c1 += 2) {
    B[c0 + 1][c1] = A[c1][c0 + 1];
    B[c0 + 1][c1 + 1] = A[c1 + 1][c0 + 1];
  }
```

## Outline

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#### Intra-Statement Consecutivity

- Consecutivity Criterion
- Specifying Schedule Constraints
- Transformation to Constraints on Schedule Coefficients
- Solving Constraints on Schedule Coefficients (isl)

#### Inter-Statement Consecutivity

### Local Rescheduling

Conclusions and Future Work

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### Local Rescheduling

Consecutivity usually only important inside tiles

- compute schedule without consecutivity (or lower priority)
- 2 tile
- recompute schedule inside tile with consecutivity

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Consecutivity usually only important inside tiles

- compute schedule without consecutivity (or lower priority)
- 2 tile
- recompute schedule inside tile with consecutivity

On trps:

```
float tmp[N][N];
for (int c0 = 0; c0 < N; c0 += 32)
for (int c1 = 0; c1 < N; c1 += 32) {
   for (int c2 = c0; c2 <= min(N - 1, c0 + 31); c2 += 1)
      for (int c3 = c1; c3 <= min(N - 1, c1 + 31); c3 += 1)
      tmp[c2][c3] = A[c2][c3];
for (int c2 = c1; c2 <= min(N - 1, c1 + 31); c2 += 1)
   for (int c3 = c0; c3 <= min(N - 1, c0 + 31); c3 += 1)
      C[c2][c3] = tmp[c3][c2];
}</pre>
```

## Outline

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#### Intra-Statement Consecutivity

- Consecutivity Criterion
- Specifying Schedule Constraints
- Transformation to Constraints on Schedule Coefficients
- Solving Constraints on Schedule Coefficients (isl)
- Inter-Statement Consecutivity
- 4 Local Rescheduling
- 5 Conclusions and Future Work

## Conclusions and Future Work

Conclusions:

- slightly generalized criterion for consecutivity
- combining multiple references per statement
- approach for integration in Pluto-style scheduler
- implementation in isl/PPCG (branch consecutivity\_CW\_709)

Future work:

experiment and fine-tune



### References I

- Bastoul, Cédric and Paul Feautrier (2004). "More Legal Transformations for Locality". In: *Euro-Par'10 International Euro-Par conference*.
  Vol. 3149. Lecture Notes in Computer Science. Pisa, pp. 272–283. DOI: 10.1007/978-3-540-27866-5\_36.
  Bondhugula, Uday, Albert Hartono, J. Ramanujam, and P. Sadayappan
- (2008). "A practical automatic polyhedral parallelizer and locality optimizer". In: *Proceedings of the 2008 ACM SIGPLAN conference on Programming language design and implementation*. PLDI '08. Tucson, AZ, USA: ACM, pp. 101–113. DOI: 10.1145/1375581.1375595.
- Feautrier, Paul (1992). "Some Efficient Solutions to the Affine Scheduling Problem. Part I. One-dimensional Time". In: *International Journal of Parallel Programming* 21.5, pp. 313–348. DOI: 10.1007/BF01407835.
- Kandemir, Mahmut T., J. Ramanujam, and Alok N. Choudhary (1999). "Improving Cache Locality by a Combination of Loop and Data Transformation". In: *IEEE Transactions on Computers* 48.2, pp. 159–167. doi: 10.1109/12.752657.

### **References II**

- Kandemir, Mahmut T., J. Ramanujam, Alok N. Choudhary, and Prithviraj Banerjee (2001). "A Layout-Conscious Iteration Space Transformation Technique". In: IEEE Transactions on Computers 50.12, pp. 1321-1335. doi: 10.1109/TC.2001.970571. Kong, Martin, Richard Veras, Kevin Stock, Franz Franchetti, Louis-Noël Pouchet, and P. Sadayappan (2013). "When polyhedral transformations meet SIMD code generation". In: Proceedings of the 34th ACM SIGPLAN conference on Programming language design and implementation. PLDI '13. Seattle, Washington, USA: ACM, рр. 127-138. рог. 10.1145/2491956.2462187. V., Sven (2010). "isl: An Integer Set Library for the Polyhedral Model". In:
  - V., Sven (2010). Isl: An integer Set Library for the Polynedral Model . In: Mathematical Software - ICMS 2010. Ed. by Komei Fukuda, Joris Hoeven, Michael Joswig, and Nobuki Takayama. Vol. 6327. Lecture Notes in Computer Science. Springer, pp. 299–302. doi: 10.1007/978-3-642-15582-6\_49.

### References III

- V., Sven, Juan Carlos Juega, Albert Cohen, José Ignacio Gómez, Christian Tenllado, and Francky Catthoor (2013). "Polyhedral parallel code generation for CUDA". In: *ACM Trans. Archit. Code Optim.* 9.4, p. 54. doi: 10.1145/2400682.2400713.
- Vasilache, Nicolas (2007). "Scalable Program Optimization Techniques in the Polyhedral Model". PhD thesis. Université Paris Sud XI, Orsay.
  Vasilache, Nicolas, Benoît Meister, Muthu Baskaran, and Richard Lethin (2012). "Joint Scheduling and Layout Optimization to Enable Multi-Level Vectorization". In: IMPACT-2: 2nd International Workshop on Polyhedral Compilation Techniques. Paris, France.
- Wolf, Michael E. and Monica S. Lam (1991). "A Data Locality Optimizing Algorithm". In: Proceedings of the ACM SIGPLAN Conference on Programming Language Design and Implementation. PLDI '91. Toronto, Ontario, Canada: ACM, pp. 30–44. DOI: 10.1145/113445.113449.

### **References IV**

Zinenko, Oleksandr, Sven V., Chandan Reddy, Jun Shirako, Tobias Grosser, Vivek Sarkar, and Albert Cohen (2018). "Modeling the Conflicting Demands of Multi-Level Parallelism and Temporal/Spatial Locality in Affine Scheduling". In: *Proceedings of the 27th International Conference on Compiler Construction*. CC 2018. accepted.